Lampiran 4 Datasheet Sensor MPX5100dp

NXP Semiconductors
Data Sheet: Technical Data

Document Number: MPX5100 Rev. 14, 12/2018

VRoHS

Universitas

MPX5100, 0 to 100 kPa, Differential, Gauge, and Absolute, Integrated, Pressure Sensors

The MPX5100 series piezoresistive transducer is a state-of-the-art monolithic silicon pressure sensor designed for a wide range of applications, but particularly those employing a microcontroller or microprocessor with A/D inputs. This patented, single element transducer combines advanced micromachining techniques, thin-film metallization, and bipolar processing to provide an accurate, high-level, analog output signal that is proportional to the applied pressure.

Features

- 2.5% maximum error over 0 to 85 °C
- · Ideally suited for microprocessor or microcontroller-based systems
- Patented silicon shear stress strain gauge
- Available in absolute, differential and gauge configuration
- Durable epoxy unibody element
- · Easy-to-use chip carrier option

Typical applications

- · Patient monitoring
- Process control
- Pump/motor controlPressure switching
- White goods

MPX5100





MPX5100AP/GP 98ASB42796B

MPX5100DP 98ASA42797B

Small outline packages





MPXV5100DP

MPXV5100GP 98ASA99303D





MPXV5100GC6U 98ASB17757C

MPXV5100GC7U

			Orde	ring infor	mation				
Un	Shipping	Package	# of Ports		Pressure type			Device	
Device name			None	Single	Dual	Gauge	Differential	Absolute	marking
Unibody Package (M	MPX5100 Series	s)		·			10		
MPX5100AP	Tray	98ASB42796B		•				•	MPX5100AP
MPX5100DP	Tray	98ASB42797B			•		•		MPX5100DP
MPX5100GP	Tray	98ASB42796B				•			MPX5100GP
Small Outline Packa	ige (MPXV5100	Series)						0	
MPXV5100DP	Tray	98ASA99255D			•				MPXV5100DF
MPXV5100GC6U	Rail	98ASB17757C							MPXV5100G
MPXV5100GC7U	Rail	98ASB17759C				•			MPXV5100G
MPXV5100GP	Tray	98ASA99303D		•		•			MPXV5100GF

NXP reserves the right to change the detail specifications as may be required to permit improvements in the design of its products.

© 2018 NXP Semiconductors, B.V.



Universitas 100 ESA UNGGU

Contents

1	Gen	eral Description
	1.1	MPX5100AP/DP/GP Block diagram
	1.2	MPX5100AP/DP/GP Pinout (Unibody)
	1.3	MPXV5100DP/GC6U/GC7U/GP block diagram
	1.4	MPXV5100DP/GC6U/GC7U/GP pinout (small outline package)
2	Mec	hanical and Electrical Specifications.
	2.1	Maximum ratings.
	2.2	Operating characteristics
3	On-	chip Temperature Compensation and Calibration
4	Pacl	rage Information.
	4.1	Pressure (P1)/Gauge (P2) side identification table.
	4.2	Minimum recommended footprint for surface mounted applications
	4.3	Package dimensions.
5	Revi	sion History



Related Documentation

The MPX5100 device features and operations are described in a variety of reference manuals, user guides, and application notes. To find the most-current versions of these documents:

1. Go to the NXP homepage at:

http://www.nxp.com/

- In the Keyword search box at the top of the page, enter the device number MPX5100.
 In the Refine Your Result pane on the left, click on the Documentation link.

MPX5100

Sensors NXP Semiconductors, B.V.



101

1 General Description

1.1 MPX5100AP/DP/GP Block diagram

Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip in a unibody package.

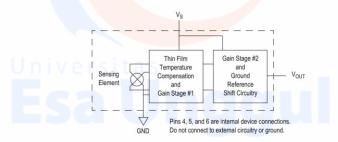


Figure 1. Integrated pressure sensor block diagram

1.2 MPX5100AP/DP/GP Pinout (Unibody)

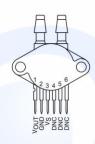


Figure 2. Device pinout (top view)

Table 1. Pin functions

Pin	Name	Function
1	V _{OUT}	Output voltage
2	GND	Ground
3	Vs	Voltage supply
4	DNC	Do not connect to external circuitry or ground.
5	DNC	Do not connect to external circuitry or ground.
6	DNC	Do not connect to external circuitry or ground.

Esa Unggul

MPX5100

Sensors NXP Semiconductors, B.V.

2



Universitas 102

Esa Unggul

1.3 MPXV5100DP/GC6U/GC7U/GP block diagram

Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip in a small outline package.

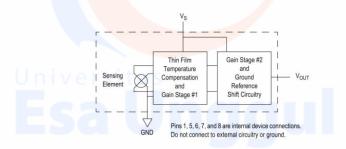


Figure 3. Integrated pressure sensor block diagram

1.4 MPXV5100DP/GC6U/GC7U/GP pinout (small outline package)

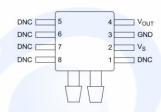


Figure 4. Device pinout (top view)

Table 2. Pin functions

Pin	Name	Function
1	DNC	Do not connect to external circuitry or ground.
2	Vs	Voltage supply
3	GND	Ground
4	V _{OUT}	Output voltage
5	DNC	Do not connect to external circuitry or ground.
6	DNC	Do not connect to external circuitry or ground.
7	DNC	Do not connect to external circuitry or ground.
8	DNC	Do not connect to external circuitry or ground.

MPX5100

Sensors NXP Semiconductors, B.V.



Universitas 103 ESG UNG G U Universita **Esa** U

2 Mechanical and Electrical Specifications

2.1 Maximum ratings

Table 3. Maximum ratings⁽¹⁾

Rating	Symbol	Value	Unit
Maximum pressure	P _{max}	400	kPa
Storage temperature	T _{stg}	-40 to +125	°C
Operating temperature	TA	-40 to +125	°C

Exposure beyond the specified limits may cause permanent damage or degradation to the device.

Operating characteristics

Table 4. Operating characteristics (V_S = 5 Vdc, T_A = 25 °C.)

Characteristic	Symbol	Min	Тур	Max	Unit
Pressure range ⁽¹⁾ Gauge, differential: MPX5100G/MPXV5100G Absolute: MPX5100AP	P _{OP}	0 15	_	100 115	kPa
Supply voltage ⁽²⁾	Vs	4.75	5.0	5.25	V _{DC}
Supply current	I _O		7.0	10	mAdc
Minimum pressure offset ⁽³⁾ , (0 to 85 °C) @ $V_S = 5.0 \text{ V}$	V _{OFF}	0.088	0.20	0.313	V _{DC}
Full-scale output ⁽⁴⁾ , differential and absolute (0 to 85 °C) @ V _S = 5.0 V	V _{FSO}	4.587	4.700	4.813	V _{DC}
Full-scale Span ⁽⁵⁾ , differential and absolute (0 to 85 °C) @ V _S = 5.0 V	V _{FSS}	7 -	4.500	-	V _{DC}
Accuracy ⁽⁶⁾	1		V=0	±2.5	%V _{FSS}
Sensitivity	V/P	-	45	-	mV/kPa
Response time ⁽⁷⁾	t _R	n—	1.0	_	ms
Output source current at full-scale output	I _{O+}	8 -8	0.1	-	mAdc
Warm-up time ⁽⁸⁾	_	_	20	_	ms
Offset stability ⁽⁹⁾	_	3 <u>—</u>	±0.5	-	%V _{FSS}

- 1. 1.0 kPa (kiloPascal) equals 0.145 psi.
- 2. Device is ratiometric within this specified excitation range.
- 3. Offset (V_{OFF}) is defined as the output voltage at the minimum rated pressure.
- Full-scale output (V_{FSO}) is defined as the output voltage at the maximum or full-rated pressure.
- Full-scale span (V_{FSS}) is defined as the algebraic difference between the output voltage at full-rated pressure and the output voltage at the minimum rated pressure.

Accuracy (error budget) consists of the following:
 Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.

Temperature hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.

Output deviation at any pressure within the specified range, when this pressure is cycled to and from minimum or maximum rated pressure at 25 °C. Pressure hysteresis:

TcSpan:

Output deviation over the temperature range of 0 to 85 °C, relative to 25 °C.

Output deviation with minimum pressure applied over the temperature range of 0 to 85 °C, relative to 25 °C. TcOffset:

 $\label{eq:Variation from nominal} Variation from nominal \ values, for offset or full-scale \ span, as a percent of \ V_{FSS} \ at 25 \ ^{\circ}C.$

- 7. Response time is defined as the time for the incremental changed in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- 8. Warm-up time is defined as the time required for the product to meet the specified output voltage after the pressure has been stabilized.
- 9. Offset stability is the product's output deviation when subjected to 1000 hours of pulsed pressure, temperature cycling with bias test.

MPX5100

Sensors NXP Semiconductors, B.V.





3 On-chip Temperature Compensation and Calibration

Figure 5 shows the sensor output signal relative to pressure input. Typical, minimum, and maximum output curves are shown for operation over a temperature range of 0 to 85 °C using the decoupling circuit shown in Figure 7. The output will saturate outside of the specified pressure range.

Figure 6 illustrates both the Differential/Gauge and the Absolute Sensing Chip in the basic chip carrier. A fluorosilicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the sensor diaphragm

The MPX5100 series pressure sensor operating characteristics, and internal reliability and qualification tests are based on use of dry air as the pressure media. Media, other than dry air, may have adverse effects on sensor performance and long-term reliability. Contact the factory for information regarding media compatibility in your application.

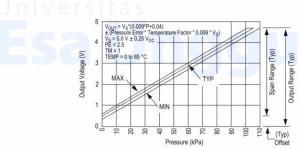


Figure 5. Output versus pressure differential

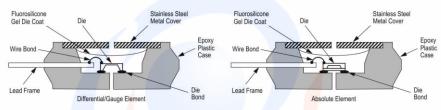


Figure 6. Cross-sectional diagrams (not-to-scale)

Figure 7 shows the recommended decoupling circuit for interfacing the output of the integrated sensor to the A/D input of a microprocessor or microcontroller. Proper decoupling of the power supply is recommended.

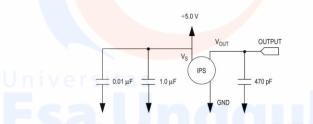


Figure 7. Recommended power supply decoupling and output filtering (For additional output filtering, please refer to application note AN1646.)

MPX5100

Sensors NXP Semiconductors, B.V. Esa l

Iniversita **Esa**

Universitas 105
ESa Unggul

The following figures show the nominal transfer function, temperature and pressure error over the operating range for the MPX5100D,MPX5100G and MPXV5100G devices.

 $\begin{aligned} \textbf{Nominal Transfer Value:} & \quad V_{OUT} = V_S \left(P \times 0.009 + 0.04\right) \\ & \quad \pm \left(\text{Pressure Error x Temp. Mult. x 0.009 x V}_S\right) \\ & \quad V_S = 5.0 \ V \pm 0.25 \ V \end{aligned}$

Figure 8. Transfer function (MPX5100D, MPX5100G, MPXV5100G)

Figure 9. Temperature error multiplier (MPX5100D, MPX5100G, MPXV5100G)

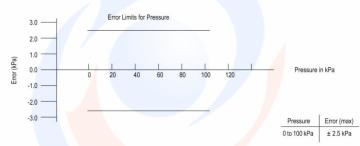


Figure 10. Pressure error band (MPX5100D, MPX5100G, MPXV5100G)

Iniversitas Esa Unggul

MPX5100

Sensors NXP Semiconductors, B.V.

versitas 106

Universita **Esa**

Esa l

Universita Esa

The following figures show the nominal transfer function, temperature and pressure error over the operating range for the MPX5100AP device.

 $\begin{tabular}{ll} \textbf{Nominal Transfer Value:} & V_{OUT} = V_S (P \times 0.009 \cdot 0.095) \\ & \pm (Pressure Error x Temp. Mult. \times 0.009 \times V_S) \\ & V_S = 5.0 \ V \pm 0.25 \ V \end{tabular}$

Figure 11. Transfer function (MPX5100AP)

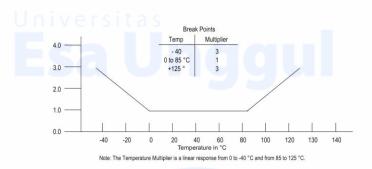


Figure 12. Temperature error multiplier (MPX5100AP)

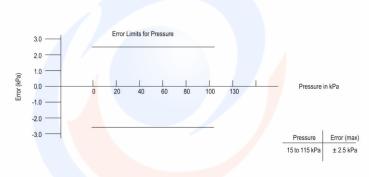


Figure 13. Pressure error band (MPX5100AP)

Esa Unggul

MPX5100

Sensors NXP Semiconductors, B.V. Universita

Esa Unggu

4 Package Information

4.1 Pressure (P1)/Gauge (P2) side identification table

NXP designates the two sides of the pressure sensor as the Pressure (P1) side and the Gauge (P2) side. The Pressure (P1) side is the side containing fluoro-silicone gel which protects the die from harsh media. The MPX pressure sensor is designed to operate with positive differential pressure applied, P1 > P2.

The Pressure (P1) side may be identified by using the table below.

Part number	Package	Pressure (P1) side identifier
MPX5100AP, MPX5100GP	98ASB42796B	Side with port attached
MPX5100DP	98ASB42797B	Side with part marking
MPXV5100DP	98ASA99255D	Side with part marking
MPXV5100GC6U	98ASB17757C	Side with port attached
MPXV5100GC7U	98ASB17759C	Side with port attached
MPXV5100GP	98ASA99303D	Side with port attached

4.2 Minimum recommended footprint for surface mounted applications

Surface mount board layout is a critical portion of the total design. The footprint for the surface mount packages must be the correct size to ensure proper solder connection interface between the board and the package. With the correct footprint, the packages will self align when subjected to a solder reflow process. It is always recommended to design boards with a solder mask layer to avoid bridging and shorting between solder

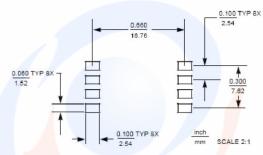


Figure 14. Small outline package footprint

4.3 Package dimensions

This drawing is located at http://cache.NXP.com/files/shared/doc/package_info/98ASB17757C.pdf.

Iniversitas Esa Unggul

MPX5100

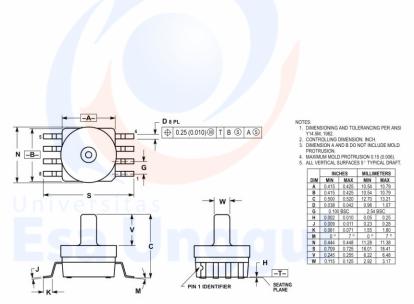
Sensors NXP Semiconductors, B.V.

9

Esa (

Universitas 108
ESO UNO O U

UNIVERSITAS ESA UNGGUL



Case 98ASB17757C, 8-lead small outline package

Universita **Esa** (



Iniversitas Esa Unggul Universita

MPX5100

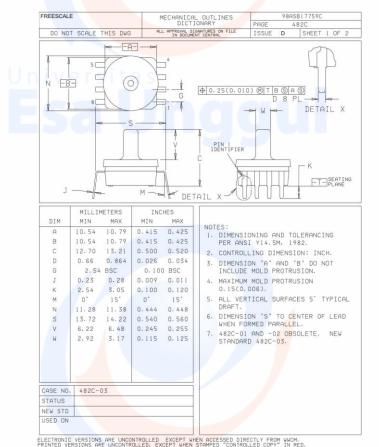
Sensors

NXP Semiconductors, B.V.



Universitas 109 ESG Ung C U

This drawing is located at http://cache.NXP.com/files/shared/doc/package_info/98ASB17759C.pdf.



Case 98ASB17759C, 8-lead small outline package

MPX5100

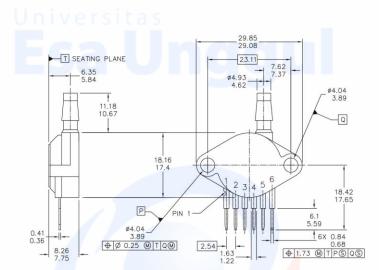
Sensors NXP Semiconductors, B.V.



110

This drawing is located at http://cache.NXP.com/files/shared/doc/package_info/98ASB42796B.pdf.

gul



© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED. MECHANICA	L OUTLINE	PRINT VERSION NO	T TO SCALE
TITLE:	DOCUMENT NO	98ASB42796B	REV: G
SENSOR, 6 LEAD UNIBODY CELL,	CASE NUMBER	28 JUL 2005	
AP & GP 01ASB09087B	STANDARD: NO	N-JEDEC	

Universitas

Case 98ASB42796B. 6-lead unibody package

MPX5100

Sensors 2 NXP Semiconductors, B.V.



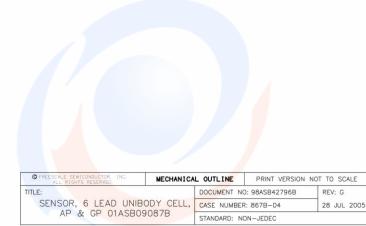
Universitas 111 **Esa Unggu**

Esa L

Universita



Universita **Esa** (



Case 98ASB42796B, 6-lead unibody package

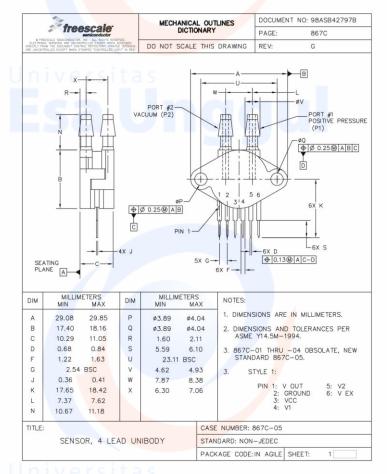
MPX5100

Sensors NXP Semiconductors, B.V.



Universitas 112 **Esa Unggu**

This drawing is located at http://cache.NXP.com/files/shared/doc/package_info/98ASB42797B.pdf.



Oniversitas

Case 98ASB42797B, 6-lead unibody package

MPX5100

Sensors

NXP Semiconductors, B.V.



Universita **Esa** (

Universita **Esa**

This drawing is located at http://cache.NXP.com/files/shared/doc/package_info/98ASA99255D.pdf.

0.180
0.180
0.180
0.180
0.160
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180
0.180

OFREESCALE SENICODUCTOR, INC.

MECHANICAL OUTLINE
PRINT VERSION NOT TO SCALE

DOCUMENT NO: 98ASA99255D
REV: A

CASE NUMBER: 1351-01
27 JUL 2005

STANDARD: NON-JEDEC

Case 98ASA99255D, 8-lead, dual port, small outline package

MPX5100

Sensors NXP Semiconductors, B.V.



Esa U

University **Esa** (

Universita **Esa**

NOTES:

- 1. CONTROLLING DIMENSION: INCH
- 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.
- DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PPROTRUSIONS. MOLD FLASH AND PROTRUSIONS SHALL NOT EXCEED .006 PER SIDE.
- A DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE .008 MAXIMUM.

Esa Unggul

Esa l



Iniversitas

Case 98ASA99255D, 8-lead, dual port, small outline package

STANDARD: NON-JEDEC

Case 30A3A332300, Great, duar port, sinan dunine package

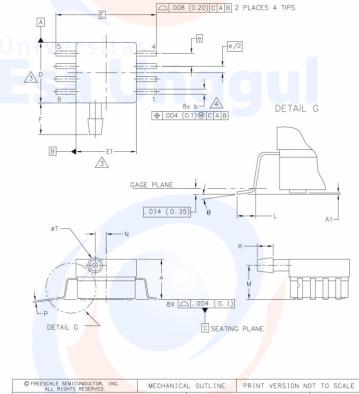
MPX5100

Sensors NXP Semiconductors, B.V.



Universitas 115 ESA UNGGU Universita

This drawing is located at http://cache.NXP.com/files/shared/doc/package_info/98ASA99303D.pdf.



TITLE:

8 LD SOP, SIDE PORT

8 LD SOP, SIDE PORT

STANDARD: NON-JEDEC

MECHANICAL OUTLINE PRINT VERSION NOT TO SCALE

DOCUMENT NO: 98ASA99303D REV: D

CASE NUMBER: 1369-01 13 DEC 2010

Case 98ASA99303D, 8-lead, side port, small outline package

PAGE 1 OF 2

MPX5100

Sensors NXP Semiconductors, B.V.



Universitas 116
ESa Ungoul

Universita **Esa** (

Universita **Esa**

Universita **ESA**

- NOTES:
 1. CONTROLLING DIMENSION: INCH
- 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.

△ DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PPROTRUSIONS.

MOLD FLASH AND PROTRUSIONS SHALL NOT EXCEED .006 (0.152) PER SIDE.

△ DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE .008 (0.203) MAXIMUM.

	INC	HES	MILL	METERS		11	NCHES	MIL	LIMETERS
DIM	MIN	MAX	MIN	MAX	DIM	MIN	MAX	MIN	MAX
А	.300	.330	7.62	8.38	θ	0.	7.	0.	7.
A1	.002	.010	0.05	0.25					
b	.038	.042	0.96	1.07	-				
D	.465	.485	11.81	12.32	-				
Ε	.717	BSC	18.3	21 BSC	-				
E1	.465	.485	11.81	12.32					
e	.100	BSC	2.5	4 BSC					
F	.245	.255	6.22	6.47	20				
К	.120	.130	3.05	3.30	-				
L	.061	.071	1.55	1.80					
М	.270	.290	6.86	7.36	-0				
N	.080	.090	2.03	2.28		1222	/	222	
Р	.009	.011	0.23	0.28	-				
Т	.115	.125	2.92	3.17	=				
© F		MICONDUCTOR, TS RESERVED.	INC.	MECHANIC	AL OUT	LINE	PRINT VERS	SION NO	T TO SCALE
TITLE;					DOCU	MENT NO	: 98ASA99303	BD	REV: D
	8 LI	SOP, S	IDE PO	RT	CASE	NUMBER	R: 1369-01		13 DEC 2010
	0 0				STAN	DARD: NO	N-JEDEC		

Case 98ASA99303D, 8-lead, side port, small outline package

MPX5100

Sensors NXP Semiconductors, B.V.



117

5 Revision History

Table 5. Revision history

Revision number	Revision date Description of changes						
14	12/2018	Deleted obsolete part numbers MPX5100A and MPX5100D. Changed format to new corporate format. Added pinout for MPX5100AP/IDP/GP in Figure 2. Added pin functions for MPX5100AP/IDP/GP as Table 1. Added pinout for MPXV5100DP/GC6U/GC7U/GP as Figure 4. Added pin functions for MPXV5100DP/GC6U/GC7U/GP as Table 2. Added pin functions for MPXV5100DP/GC6U/GC7U/GP as Table 2. Deleted section 5.1, Package dimensions to Section 4.3. Deleted section 5., duplicate Package Information information section. Added revision history as Table 5.					

Universita **Esa** (



Esa Unggul

University

Sensors NXP Semiconductors, B.V. MPX5100



Universitas 118
Esa Unggul

UNIVERSITAS ESA UNGGUL

How to Reach Us:

Home Page:
NXP.com

Web Support:
NXP.com/support

Information in this document is provided solely to enable system and software implementers to use NXP products. There are no expressed or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation, consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by the customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address:

NXP.com/salestermsandconditions.

NXP and the NXP logo are trademarks of NXP Semiconductors, B.V. All other product or service names are the property of their respective owners.

NXP Semiconductors, B.V., Inc., 2018. All Rights reserved.

Esa Unggu

Document Number: MPX5100 Rev. 14



Universit



Universitas 119
Esa Unggu